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Approved for use through 10/31/2002. OMB 0651-0031

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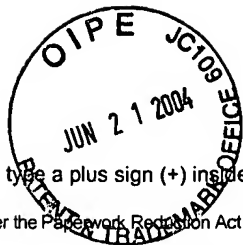
TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/602,253
	Filing Date	June 23, 2003
	First Named Inventor	Salman Akram
	Group Art Unit	2826
	Examiner Name	L. Andujar
Attorney Docket Number		2269-3091.5US (96-0890.04/US)

ENCLOSURES (check all that apply)		
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<input checked="" type="checkbox"/> Amendment in response to office action dated March 17, 2004	<input type="checkbox"/> Petition	
<input type="checkbox"/> Amendment under 37 C.F.R. § 1.116 in response to final office action dated	<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Other Enclosure(s) (please identify below):
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	Greg T. Warder	Registration No. 50,208
Signature		
Date	June 17, 2004	

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Signature		Date	June 17, 2004

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Firm or Individual name	Greg T. Warder	Registration No. 50,208
Signature		
Date	June 17, 2004	

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Salman Akram

Serial No.: 10/602,253

Filed: June 23, 2003

For: METHODS OF FABRICATING
SEMICONDUCTOR SUBSTRATE-BASED
BGA INTERCONNECTION

Confirmation No.: 1942

Examiner: L. Andujar

Group Art Unit: 2826

Attorney Docket No.: 2269-3091.5US
(96-0890.04/US)

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June 17, 2004
Date


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Leah J. Barrow
Name (Type/Print)

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Office Action mailed March 17, 2004, the three-month shortened statutory period for response to which expires on June 17, 2004.

Amendments to the Title appear on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 5 of this paper.



IN THE TITLE

The title has been amended herein. Pursuant to 37 C.F.R. §§ 1.121 and 1.125 (as amended to date), please enter the title as amended.

~~METHODS OF FABRICATING~~ SEMICONDUCTOR SUBSTRATE-BASED BGA
INTERCONNECTION FOR TESTING SEMICONDUCTOR DEVICES